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APPLICANTS

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** CONTINUING DATA ****

This application is a DIV of 10/278,496 10/23/2002 PAT 6,611,950
 which is a DIV of 10/080,740 02/22/2002 PAT 6,502,225
 which is a DIV of 09/274,659 03/23/1999 PAT 6,421,816

** FOREIGN APPLICATIONS ****

JAPAN HEI. 10-94993 04/07/1998 NO

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/24/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verifier and Acknowledged <i>[Signature]</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 3
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TITLE

Semiconductor device, semiconductor device design method, semiconductor device design method recording medium, and semiconductor device design support system

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of)
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